## OC Docket No.: INFN/0037 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAIL STOP PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **CERTIFICATE UNDER 37 CFR 1.10**

I hereby certify that this correspondence and the documents referred to as attached therein are being deposited on November 24, 2003 with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee," mailing label No. EV335472255US addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 24, 2003 Signature Date

Re:Inventor(s): TILL SCHLÖSSER and BRIAN LEE

Title: DRAM CELL ARRANGEMENT WITH VERTICAL MOS TRANSISTORS, AND METHOD FOR

	TIS FABRICATION				
Trans	mitted herewith is the patent application identified above, including:				
$\boxtimes$	Specification, claims and abstract <u>16</u> Total Pages				
$\boxtimes$	Drawings				
$\boxtimes$	Declaration and Power of Attorney (Unsigned)				
$\boxtimes$	Information Disclosure Statement with List				
	Assignment of the Invention to				
	Assignment Recordation Cover Sheet				

## **FEE CALCULATION**

	NUMBER OF CLAIMS FILED	LESS NUMBER PAID BY BASIC FEE	NUMBER OF EXTRA CLAIMS (Not less than zero)	. ENTITY FEE
Basic Fee				\$ .00
Total Claims	24	- 20 = 4	X \$18 =	\$.00
Independent Claims	4	-3=2	X \$86 =	\$.00
First Presenta	-0-			
	\$.00			

The Commissioner is hereby authorized to charge \$ to Deposit Account No. A duplicate copy of this transmittal is enclosed.

 $\Box$ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No.. A duplicate copy of this transmittal is enclosed.

 $\boxtimes$ Please address all future correspondence to:

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Respectfully submitted

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